

IN THE CLAIMS:

Kindly amend claims 1, 3-11 and 13-20 as follows. A detailed listing of all claims is as follows.

Claim 1 (Currently Amended): A method of driving a liquid crystal display, comprising:
 setting first modulated data in advance in the liquid crystal display;
 calculating a difference between the first modulated data and ~~normal~~ input data; and
 modulating the ~~normal~~ input data by using the calculated difference to output second
modulated data.

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cont
 Claim 2 (Original): The method according to claim 1, wherein the difference is an
 absolute value.

Claim 3 (Currently Amended): The method according to claim 1, ~~wherein the~~
~~calculating a difference comprises,~~ further comprising:
 adding the second modulated data and the ~~normal~~ input data; and
 performing a subtraction operation between the second modulated data and the ~~normal~~
 input data.

Claim 4 (Currently Amended): The method according to claim 3, further comprising:
 delaying the ~~normal~~ input data;
 comparing the delayed ~~normal~~ input data with the ~~normal~~ input data; and
 selecting one of the added data and the subtracted data depending on the compared result.

Claim 5 (Currently Amended): The method according to claim 4, wherein the selected data are equal to the first modulated data set in advance.

Claim 6 (Currently Amended): The method according to claim 1, wherein the ~~normal~~ input data are added with the second modulated data ~~that are generated by modulating the normal input data.~~

Claim 7 (Currently Amended): ~~[[A]] The method of driving a liquid crystal display~~ according to claim 1, further comprising:

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- dividing the ~~normal~~ input data into most significant bits and least significant bits;
- delaying the most significant bits for a frame period;
- adding the second modulated data with non-delayed most significant bits;
- performing a subtraction operation between the second modulated data and the non-delayed most significant bits;
- comparing the delayed most significant bits with the non-delayed most significant bits;
- and
- selecting one of the added data and the subtracted data depending on the compared result;

~~thereby outputting the modulated data.~~

Claim 8 (Currently Amended): The method according to claim 1, further comprising:

- dividing the ~~normal~~ input data into most significant bits and least significant bits;
- delaying the most significant bits for a frame period; and

adding non-delayed most significant bits and the second modulated data ~~generated by~~
~~modulating the normal input data~~, thereby outputting the first modulated data set in advance.

Claim 9 (Currently Amended): The method according to claim 7, wherein the second modulated data are selected in accordance with a change between the delayed data and the non-delayed data.

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Claim 10 (Currently Amended): The method according to claim 8, wherein the second modulated data are selected in accordance with a change between the delayed data and the non-delayed data.

Claim 11 (Currently Amended): A driving apparatus for a liquid crystal display, comprising:

an input line receiving ~~normal~~ input data; and

a modulator modulating the ~~normal~~ input data by using subtracted data between first modulated data set in advance and the ~~normal~~ input data from the input line to output second modulated data.

Claim 12 (Original): The driving apparatus according to claim 11, wherein the subtracted data are used as an absolute value.

Claim 13 (Currently Amended): The driving apparatus according to claim 11, further comprising:

an adder adding the second modulated data and the ~~normal~~ input data; and
a subtracter performing a subtraction operation between the second modulated data and the ~~normal~~ input data.

Claim 14 (Currently Amended): The driving apparatus according to claim 13, further comprising:

a frame memory delaying the ~~normal~~ input data;
a comparator comparing the ~~normal~~ input data with the delayed ~~normal~~ input data for a frame period; and
a selector selecting one of the added data and the subtracted data depending on the compared result from the comparator.

Claim 15 (Currently Amended): The driving apparatus according to claim 14, wherein the selected data are equal to the first modulated data set in advance.

Claim 16 (Currently Amended): The driving apparatus according to claim 11, further comprising an adder adding the second modulated data with the ~~normal~~ input data to output the first modulated data set in advance.

Claim 17 (Currently Amended): The driving apparatus according to claim 11, further comprising:

a frame memory delaying most significant bits of the ~~normal~~ input data;
an adder adding the second modulated data and non-delayed most significant bits;

a subtracter performing a subtraction operation between the second modulated data and the non-delayed most significant bits;

a comparator comparing the delayed most significant bits with the non-delayed most significant bits; and

a selector selecting one of the added data and the subtracted data depending on the compared result.

Claim 18 (Currently Amended): The driving apparatus according to claim 11, further comprising:

a frame memory delaying most significant bits of the ~~normal~~ input data; and

an adder adding the second modulated data with the non-delayed most significant bits to output the first modulated data set in advance.

Claim 19 (Currently Amended): The driving apparatus according to claim 17, wherein the second modulated data are selected in accordance with a change between the delayed data and the non-delayed data.

Claim 20 (Currently Amended): The driving apparatus according to claim 18, wherein the second modulated data are selected in accordance with a change between the delayed data and the non-delayed data.
